

## LINKING HARDWARE AND SOFTWARE FOR FRUGAL AI SOLUTIONS

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Resistive random access memory (RRAM) technologies, often referred to as memristors, hold fantastic promise for implementing novel in-memory computing systems for massively parallel, low-power and low-latency computation.

This talk will first present the role of RRAM to enable the hardware implementation of Spiking Neural Networks (SNN). Second, we will present different approaches to compute in-memory with imperfect devices and without error correction codes, going from relatively conventional approaches to radical ideas exploiting device imperfections.